



SAMSUNG DISPLAY

Product Specification

(☒) Preliminary Specification
(☐) Approval Specification

The information described in this SPEC is preliminary and can be changed without prior notice

CUSTOMER	HISENSE	MODEL NO.	LTA230AN01
DATE OF ISSUE	12/12/20	EXTENSION CODE	

Customer Approval & Feedback

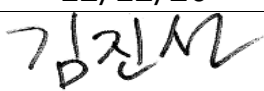
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REVISION HISTORY

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GENERAL DESCRIPTION

DESCRIPTION

The LTA230AN01 is the one of liquid crystal display devices(LCD) that uses an amorphous silicon TFT(Thin Film Transistor) as switching components and a color active matrix. This model is composed of a TFT LCD panel, a driver circuit, and a back-light unit. This 23.0" model has a resolution of 1366 x 768 pixels and can display up to 16.7Million colors with the wide viewing angle of 89° or higher in all directions.

FEATURES

RoHS compliance(Pb-Free)
High contrast ratio & aperture ratio with the wide color gamut
SVA(Super Vertical align) mode
Wide Viewing angle($\pm 178^\circ$)
High speed response
HD resolution(16:9)
Low Power consumption
wLED 1side edge BLU
DE(Data enable) mode
The interface(1pixel/clock) of 1ch LVDS(Differential signaling with a low voltage)

APPLICATIONS

Home-alone Multimedia TFT-LCD TV
High Definition TV

GENERAL INFORMATION

Items	Specification	Unit	Note
Module Size	534.0 (H) x 311.8 (V)	mm	± 0.5
Weight	11.1 (Dmax) 2100	g	w/o converter max
Pixel Pitch	0.372 x 0.372	mm	
Active Display Area	508.152(H) X 285.696 (V)	mm	
Surface Treatment	Anti Glare, Haze 2.3%		$\pm 2.1\%$
Display Colors	8bit - 16.7M	colors	
Number of Pixels	1366 x 768	pixel	16 : 9
Pixel Arrangement	RGB vertical stripe		
Display Mode	Normally Black		
Luminance of White	250	cd/m ²	

MECHANICAL INFORMATION

Item		Min.	Typ.	Max.	Note
Module size	Horizontal(H)	533.5	534.0	534.5	mm
	Vertical(V)	311.3	311.8	312.3	mm
	Depth(D)	10.1	10.6	11.1	mm * note (1)
Weight				2100	g

NOTE (1) top chassis to bottom surface

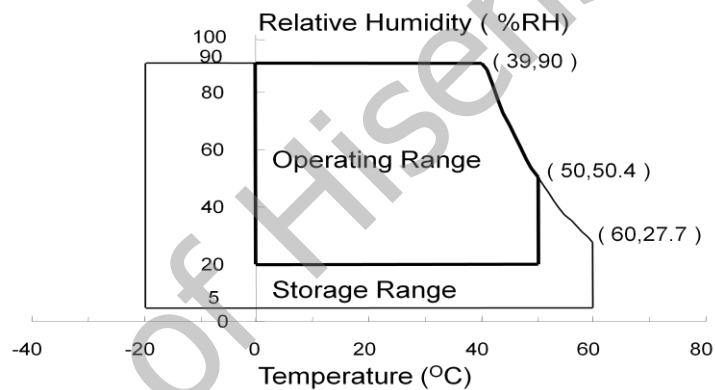
For eyes of Hisense only

1. ABSOLUTE MAXIMUM RATINGS

1.1 ENVIRONMENTAL ABSOLUTE RATINGS

Item	Symbol	Min.	Max.	Unit	Note
Power supply voltage	VDD	GND-0.3	13.2	V	(1)
Storage temperate	TSTG	-20	60	°C	(1)
Operating temperature	TOPR	0	50	°C	(1)
Surface temperature	TSUR	0	65	°C	(2)
Shock (non-operating)	Snop(X,Y,Z)	-	50	G	(3), (5)
Vibration (non-operating)	Vnop		1.5	G	(4), (5)

Note (1) The ranges of temperature and relative humidity are shown in the graph below. 90% RH Max.
 (The temperature of Ta shall be over 39 °C.)
 The maximum temperature of wet-bulb shall be less than 39 °C.
 No condensation



Note (2) Although abnormal visual problems can be occurred in TSUR range, the polarizer is not damaged in this range.

Note (3) 20ms, one time for $\pm X$, $\pm Y$, $\pm Z$ axis

Note (4) 10-300 Hz, Sweep rate 11min, 30min for X,Y,Z axis

Note (5) The fixture for the test of the vibration and shock, which holds the module to be tested shall be hard and rigid in order for the module not to be twisted or bent by the fixture.

2. OPTICAL CHARACTERISTICS

The optical characteristics shall be measured in the dark room or the space surrounded by the similar ambient setting.

Measuring equipment : TOPCON RD-80S, TOPCON SR-3, ELDIM EZ-Contrast

$T_a = 25 \pm 2 \text{ }^{\circ}\text{C}$, $V_{DD} = 12\text{V}$, $f_v = 60\text{Hz}$, $f_{DCLK} = 78\text{MHz}$, $IF = 100\%$ duty

Item		Sym bol	Condition	Min.	Typ.	Max.	Unit	Note	
Contrast Ratio		CR		3000	4000	-	-	(1) SR-3	
Respo nse time	G-to-G	T _g	T _{PAN.SUR} = 29.9℃	-	8	16	msec	(3) RD-80S	
Luminance of White (At the center of screen)		Y _L	Normal ϕ = 0 θ = 0 Viewing Angle	200	250	-	cd/m ²	(4) SR-3	
Color Chromat icity (CIE 1931)	Red	R _x		-0.03	0.645	+0.03	-	(5),(6) SR-3	
		R _y							0.338
	Green	G _x							0.302
		G _y							0.603
	Blue	B _x							0.149
		B _y							0.067
	White	W _x							0.285
		W _y							0.311
Color Gamut		-		-	-	68	-	%	(5) SR-3
Color temperature		-	-	-	8500	-	K		
Viewing Angle	Hor.	θ _L	CR ≥ 10	70	85	-	Degree	(6) SR-3 EZ-Contrast	
		θ _R		70	85	-			
	Ver.	ϕ _U		70	80	-			
		ϕ _D		70	80	-			
Brightness Uniformity (9 Point)		B _{uni}	-	-	-	25	%	(2) SR-3	

- Test equipment for setup

The measurement shall be executed under the condition including a stable, windless and dark room for 40min or 60min with lighting the back-light at the given temperature, which is suitable to stabilize the back-light.

The module shall be measured at the center of screen.

The ideal temperature for setup shall be the value derived from the formula, $T_a = 25 \pm 2 \text{ }^{\circ}\text{C}$.

Note (1) Definition of Viewing angle : The range of Viewing angle ($10 \leq C/R$)

: Ratio of max. gray (Gmax) & min. gray (Gmin) at the center point of the panel

$$C/R = \frac{G_{\max}}{G_{\min}}$$

Gmax : Luminance in all white pixels

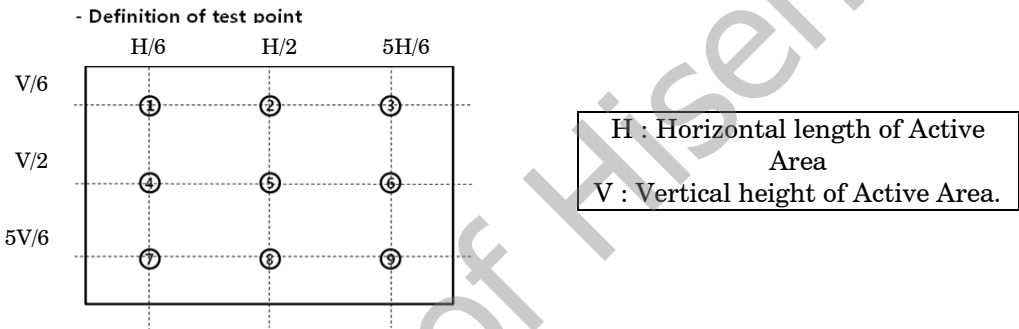
Gmin : Luminance in all black pixels.

Note (2) Definition of brightness uniformity at 9 points (Test pattern : Full white)

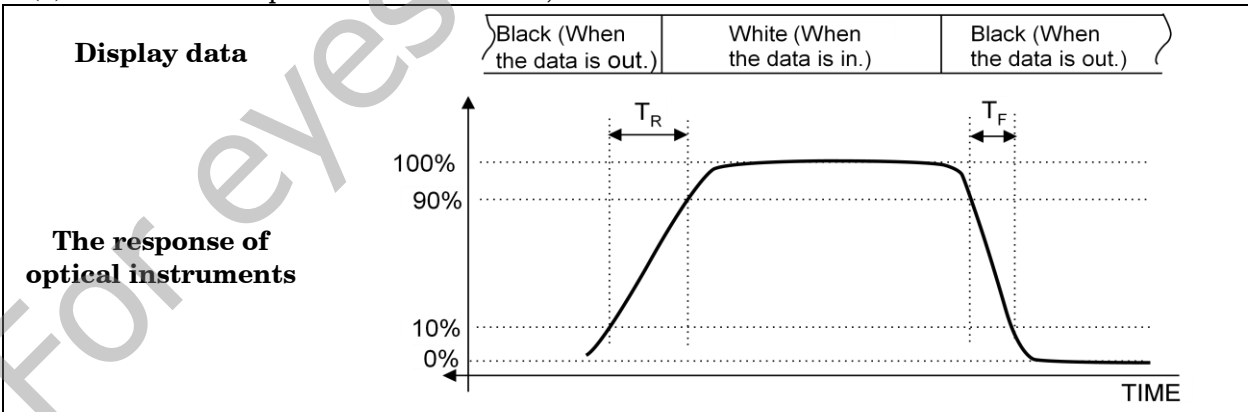
$$B_{uni} = 100 * \frac{(B_{\max} - B_{\min})}{B_{\max}}$$

Bmax : Maximum brightness

Bmin : Minimum brightness



Note (3) Definition of response time : Sum of T_r , T_f

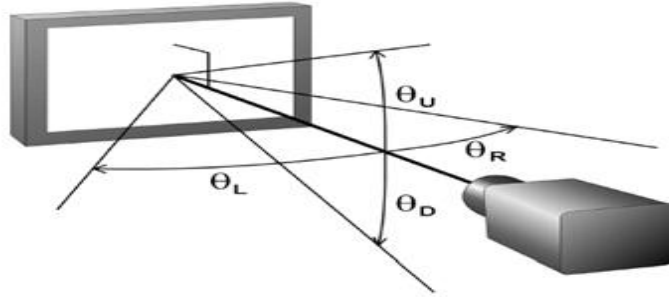


※ G-to-G : Average response time between the whole gray scale to the whole gray scale.

Note (4) The definition of luminance of white: The luminance of white at the center point ⑤

Note (5) The definition of chromaticity (CIE 1931)

The color coordinate of red, green, blue and white at the center point ⑤



Note (6) Definition of viewing angle : The range of viewing angle ($C/R \geq 10$)

3. ELECTRICAL CHARACTERISTICS

3.1 TFT LCD MODULE

The connector to transmit a display data and a timing signal shall be connected.

* $T_a = 25 \pm 2^\circ\text{C}$

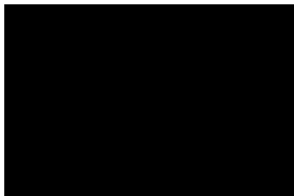
Item		Symbol	Min.	Typ.	Max.	Unit	Note
Voltage of Power Supply		V_{DD}	10.8	12.0	13.2	V	(1)
Current of Power Supply	(a) Black	I_{DD}	-	313	413	mA	(2), (3)
	(b) White		-	305	405		
	(c) V-Stripe		-	559	659		
Vsync Frequency		f_v	50	60	66	Hz	-
Hsync Frequency		f_H	44	48	53	kHz	-
Main Frequency		F_{delk}	72	78	85	MHz	-
Rush Current		I_{RUSH}	-	-	4	A	(4)

Note (1) The voltage for ripple shall be controlled under the range, which is lower than 10% of V_{DD} voltage.

Note (2) $f_v=60\text{Hz}$, $f_{DCLK} = 78\text{MHz}$, $V_{DD} = 12.0\text{V}$, DC Current.

Note (3) The pattern for checking the power dissipation (LCD module only)

a) Black Pattern



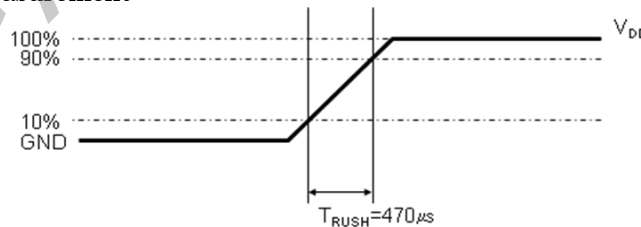
b) White Pattern



c) V-stripe



Note (4) Conditions for measurement



The rush current, I_{RUSH} can be measured when T_{RUSH} is $470\mu\text{s}$.

3.2 BACK LIGHT UNIT

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Operating Temperature range	Top	-20	-	70	°C	Note (1)
Storage Temperature range	Tstg	-30	-	85	°C	
Junction Temperature	Tj	-	-	125	°C	
LED Forward Current	IF	-	120	125	mA	1 Ch
LED Forward Voltage	VF	-	3.2	3.4	V	
Thermal Resistance Junction to PCB	Rth, JS	-	-	23	K/W	Note (1)
Power Consumption	P	-	12.7	14.0	W	IF X VF X 3ch
Operating Life Time	Hr	30,000	-	-	Hour	Note (2)
LED Counts	Q	-	33	-	EA	

Note (1) LED unit absolute rating

Note (2) It is defined as the time to take until the brightness reduces to 50% of its original value.

Operating condition : Ta = 25±2 °C, for LED package only.

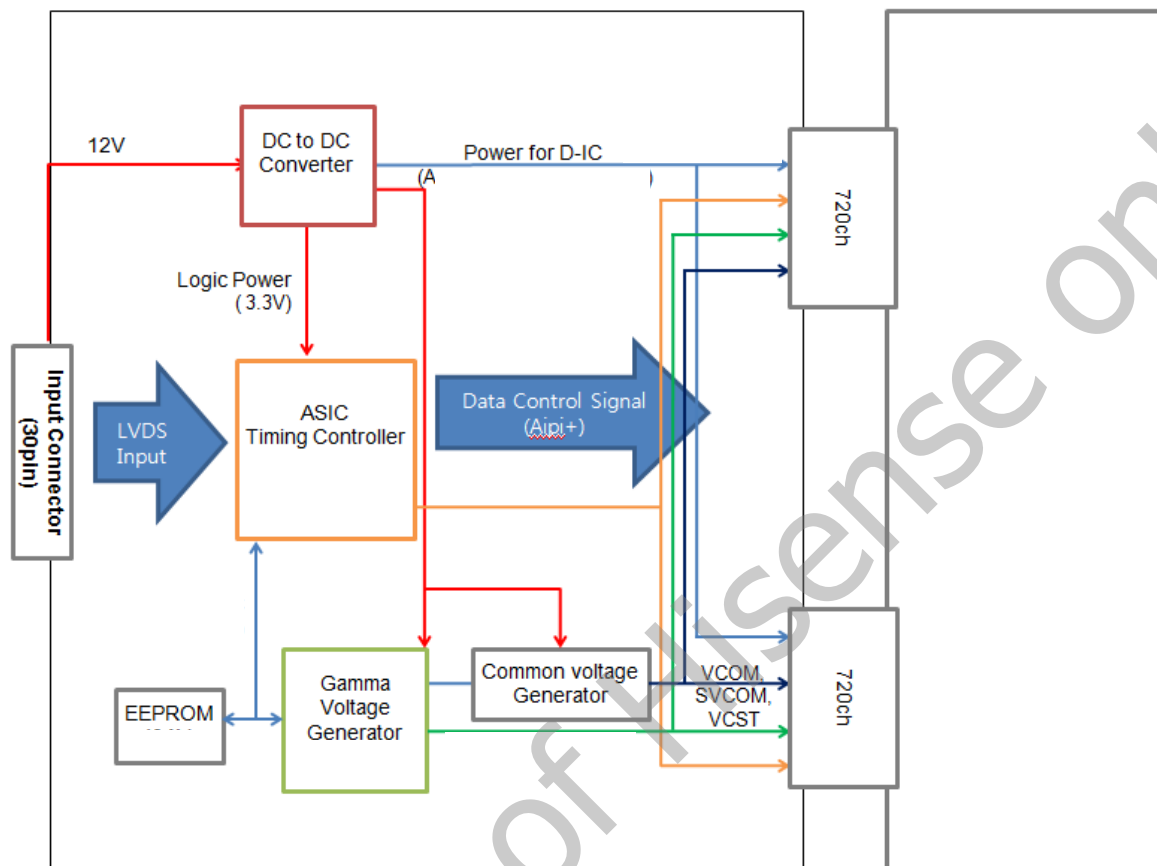
3.3 Converter connection cable

Connector 104086-0410 Molex

Pin,	Pin name	Description
1	Vin	LED power input
2	RTN1	LED bar return channel 1
3	RTN2	LED bar return channel 2
4	RTN3	LED bar return channel 3

4. BLOCK DIAGRAM

4.1 TFT LCD MODULE



5. INPUT TERMINAL PIN ASSIGNMENT

5.1 INPUT SIGNAL & POWER

Connector : IS100-L300-C23

INPUT CONNECTOR PIN MAP			
NO	PIN	NO	PIN
1	VIN	16	LV1_P
2	VIN	17	GND
3	VIN	18	LV2_N
4	VIN	19	LV2_P
5	NC	20	GND
6	GND	21	LVCLK_N
7	GND	22	LVCLK_P
8	NC	23	GND
9	LVDS_SEL note(2)	24	LV3_N
10	NC	25	LV3_P
11	GND	26	GND
12	LV0_N	27	NC
13	LV0_P	28	NC
14	GND	29	NC
15	LV1_N	30	GND

NOTE(1) NC : Not connection, PINs are used SDC only.

NOTE(2) LVDS Option : If this pin is high (3.3V) → Normal LVDS format
Low (GND) → JEIDA LVDS format

Note (3) : Pin number, which starts from the left side.

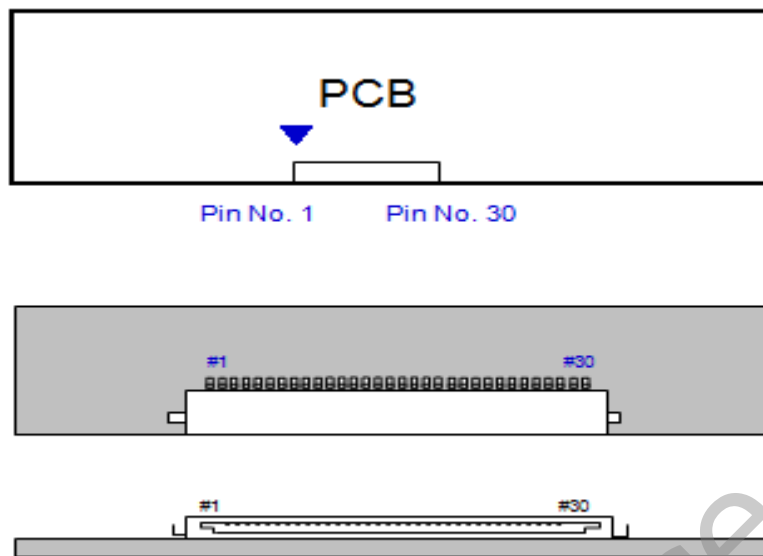


Fig. Connector diagram

- a. Pins for power GND shall be connected to the LCD's metal chassis.
- b. All input pins for power shall be connected together.
- c. All NC pins shall be designed with being separated from other signal or power.

5.2 LVDS INTERFACE

- LVDS Receiver : T-CON (merged)
- Data Format(JEIDA & VESA)

	LVDS pin	JEIDA -DATA	VESA -DATA
TxOUT/RxIN0	TxIN/RxOUT0	R2	R0
	TxIN/RxOUT1	R3	R1
	TxIN/RxOUT2	R4	R2
	TxIN/RxOUT3	R5	R3
	TxIN/RxOUT4	R6	R4
	TxIN/RxOUT6	R7	R5
	TxIN/RxOUT7	G2	G0
TxOUT/RxIN1	TxIN/RxOUT8	G3	G1
	TxIN/RxOUT9	G4	G2
	TxIN/RxOUT12	G5	G3
	TxIN/RxOUT13	G6	G4
	TxIN/RxOUT14	G7	G5
	TxIN/RxOUT15	B2	B0
	TxIN/RxOUT18	B3	B1
TxOUT/RxIN2	TxIN/RxOUT19	B4	B2
	TxIN/RxOUT20	B5	B3
	TxIN/RxOUT21	B6	B4
	TxIN/RxOUT22	B7	B5
	TxIN/RxOUT24	HSYNC	HSYNC
	TxIN/RxOUT25	VSYNC	VSYNC
	TxIN/RxOUT26	DEN	DEN
TxOUT/RxIN3	TxIN/RxOUT27	R0	R6
	TxIN/RxOUT5	R1	R7
	TxIN/RxOUT10	G0	G6
	TxIN/RxOUT11	G1	G7
	TxIN/RxOUT16	B0	B6
	TxIN/RxOUT17	B1	B7
	TxIN/RxOUT23	RESERVED	RESERVED

5.3 INPUT SIGNALS, BASIC DISPLAY COLORS AND GRAY SCALE

COLOR	DISPLAY (8bit)	DATA SIGNAL																												GRAY SCALE LEVEL
		RED									GREEN									BLUE										
		R0	R1	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	G7	B0	B1	B2	B3	B4	B5	B6	B7					
BASIC COLOR	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-			
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	-			
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	-				
	CYAN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-				
	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-				
	MAGENTA	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	-				
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	-				
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-				
GRAY SCALE OF RED	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0				
	DARK ↑ ↓ LIGHT	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1				
		0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2				
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	R3~ R252				
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:						
		1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R253				
		0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R254				
	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R255				
	GRAY SCALE OF GREEN	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0			
DARK ↑ ↓ LIGHT		0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G1				
		0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G2				
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	G3~ G252				
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:						
		0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G253				
		0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G254				
GREEN		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	G255					
GRAY SCALE OF BLUE	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	B0					
	DARK ↑	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	B1				
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	B2				

BLUE	↓ LIGHT	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	B3~ B252
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	B253
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	B254
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	B255

Note (1) Definition of gray : Rn: Red gray, Gn: Green gray, Bn: Blue gray (n=gray level)

Note (2) Input signal: 0 =Low level voltage, 1=High level voltage

6. INTERFACE TIMING

6.1 THE PARAMETERS OF TIMING(DE mode)

SIGNAL	ITEM	SMBOL	MIN.	TYP.	MAX.	Unit	NOTE
Clock	Frequency	$1/T_C$	72	78	85	MHz	-
Hsync		F_H	44	48	53	KHz	-
Vsync		F_V	50	60	66	Hz	-
Term for the vertical display	Active display period	T_{VD}	-	768	-	Lines	-
	Total vertical	T_V	775	802	1200	Lines	-
Term for the horizontal display	Active display period	T_{HD}	-	1366	-	Clocks	-
	Total Horizontal	T_H	1460	1624	2000	clocks	-

Note) The signals of Hsync and Vsync must be inputted even though this T-con is operated with DE signal only.

(1) Test Point: TTL controls signals and CLKs at LVDS Tx at the input terminal of system.

(2) Internal VDD = 3.3V

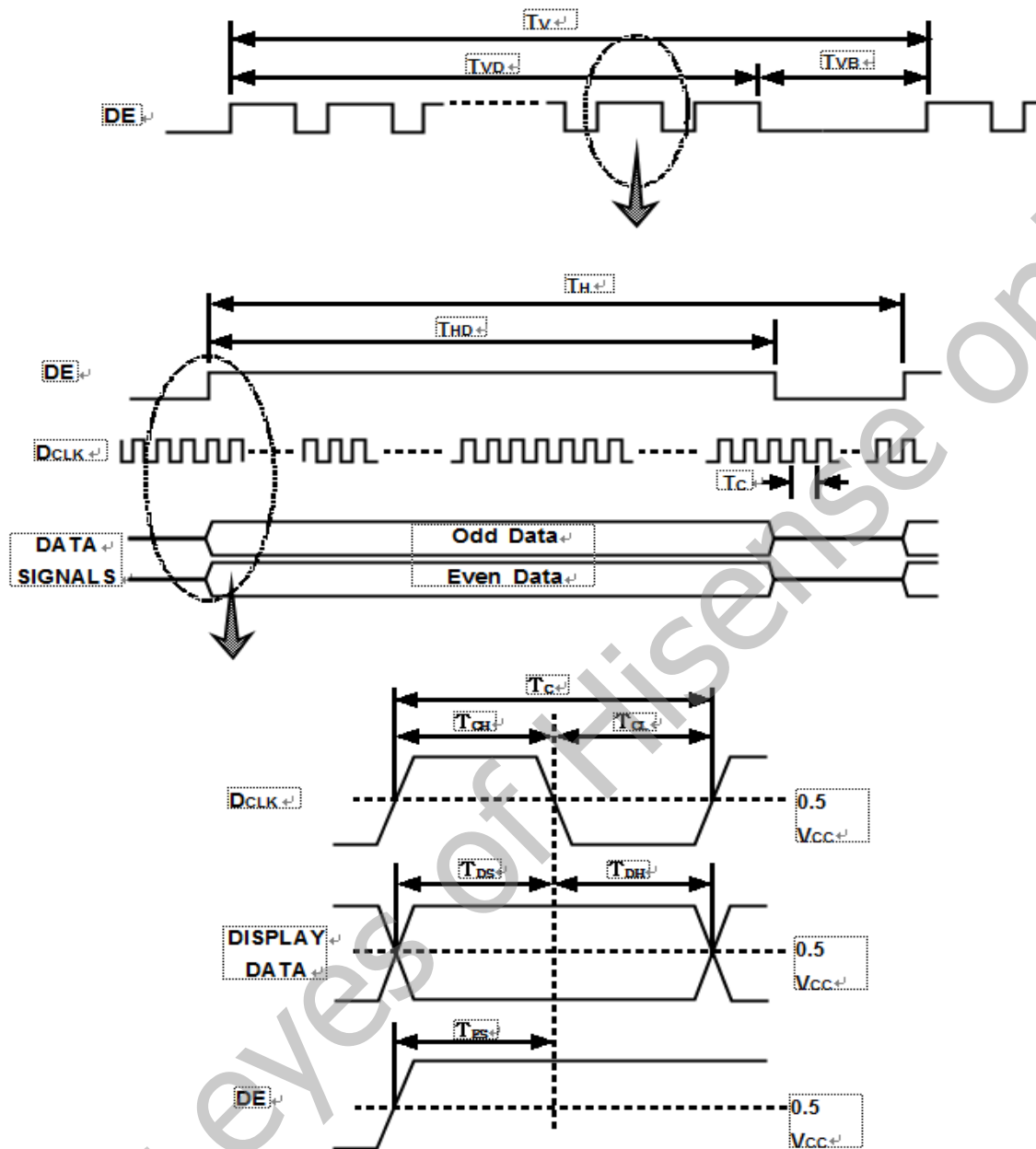
(3) The spread spectrum

- The limit of spread spectrum's range of SET in which the LCD module is assembled should be within $\pm 3\%$

- Frequency for modulation : Min 30KHz

Parameter		Symbol	Value			Unit	Note
			Min	Typ	Max		
CMOS Interface	Input High Threshold Voltage	V_{IH} (High)	2.5	-	3.3	V	
	Input Low Threshold Voltage	V_{IL} (Low)	0	-	0.5	V	

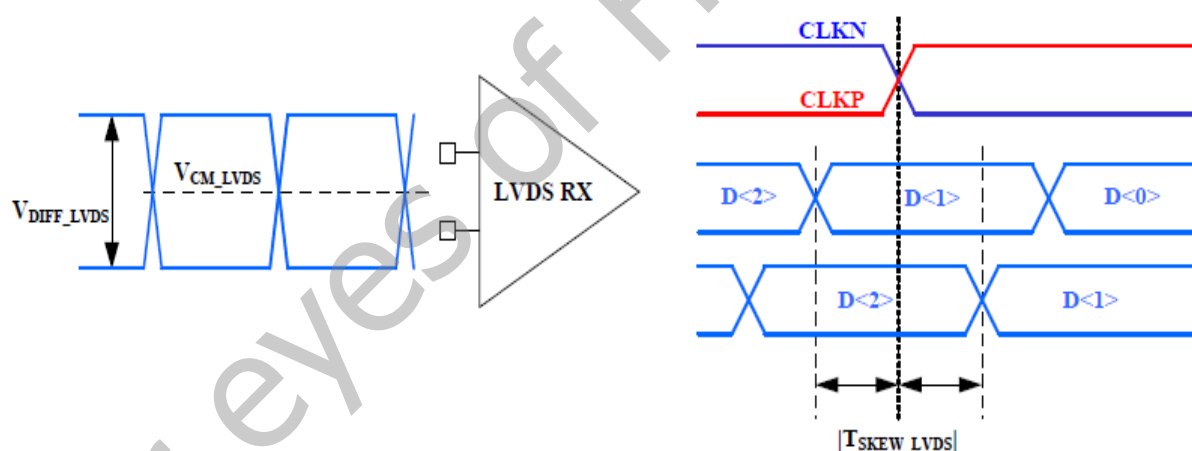
6.2 TIMING DIAGRAMS OF INTERFACE SIGNAL(Only DE mode)



6.3 CHARACTERISTICS OF INPUT DATA OF LVDS

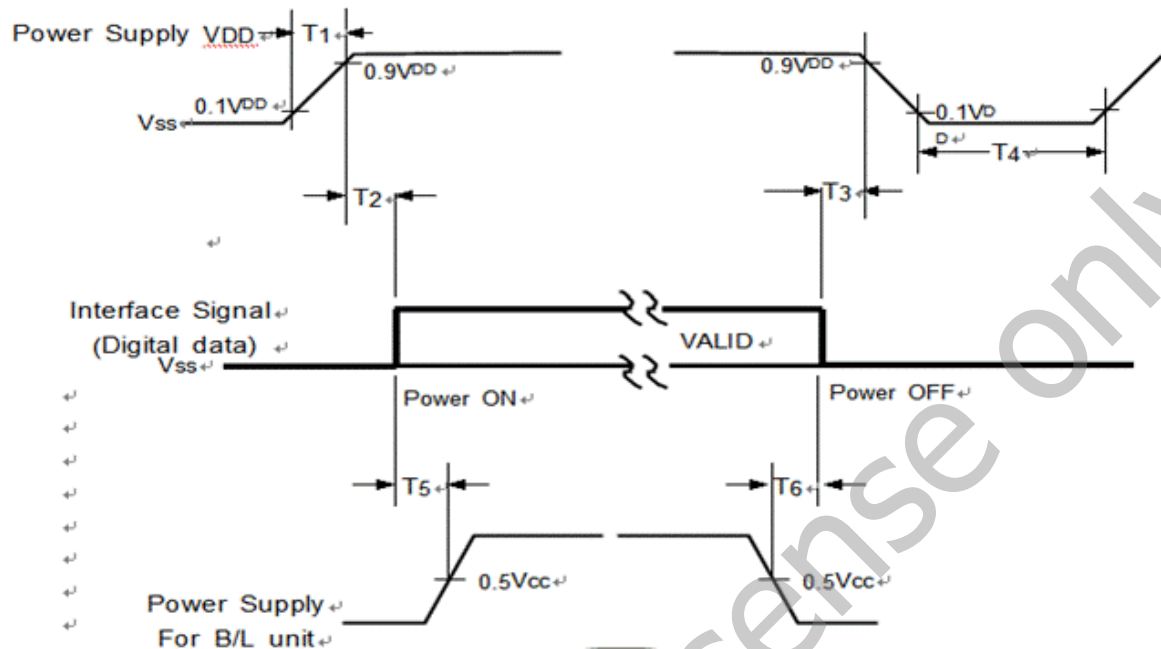
(1) LVDS characteristics

	ITEM	SYMBOL	Min	Typ	Max	Unit	Note
DC characteristics	Supply voltage	V_{DD}	3.0	3.3	3.6	V	
	Input Common mode Voltage	V_{CM}	0.3		1.8	V	
	Differential Input Voltage	$ V_{ID} $	100		600	mV	
AC characteristics	Input data position	T_{RSRM}	-	-	450	ps	$F_{IN} = 78\text{MHz}$
		T_{RSLM}	-450	-	-	ps	



6.4 THE SEQUENCE OF POWER ON AND OFF

To prevent the product from being latched up or the DC in the LCD module from starting an operation, the order to turn the power on and off should be changed to the order as shown in the diagram below.



Timing	Spec	Remarks
T_1	$0.5 \text{ msec} < T_1 \leq 10 \text{ msec}$	The time, during which the level of V_{DD} is rising from 10% to 90%.
T_2	$10 \text{ msec} < T_2 \leq 50 \text{ msec}$	The changing time, during which the V_{DD} starts rising beyond 90% until the valid data of signal started coming in.
T_3	$0 \text{ msec} < T_3 \leq 50 \text{ msec}$	The changing time, during which the valid data of signal starts leaving out until the V_{DD} starts falling below 90%.
T_4	$1000 \text{ msec} \leq T_4$	The changing time, during which the V_{DD} starts falling below 10% to restart the Windows.
T_5	$1000 \text{ msec} \leq T_5$ (BLU ON TIME)	The changing time, during which the signal of BLU starts rising beyond 50%.
T_6	$100 \text{ msec} \leq T_6$	The changing time, during which the signal of BLU starts falling below 50%.

- The inputted V_{DD} 's value for supply voltage, BLU, and signal to the external system of the module shall be computed with referring to the former mentioned value.
- The method to apply the voltage to the lamp within the range, which the LCD operates. When the back-light is turned on before the LCD is operated or the power of LCD is turned off before the back-light is turned off, the abnormal display on the screen may be shown momentarily.
- Please keep the level of input signal low or keep the level of impedance high when the value of V_{DD} is below 10%.
- The value shall be measured after the module has been fully discharged between the period, which the power is turned on and the period, which the power is turned off like the T_4 timing. The backlight may be flashed if the interface signal remains floated when the above-mentioned signal becomes invalid.

7. OUTLINE DIMENSION

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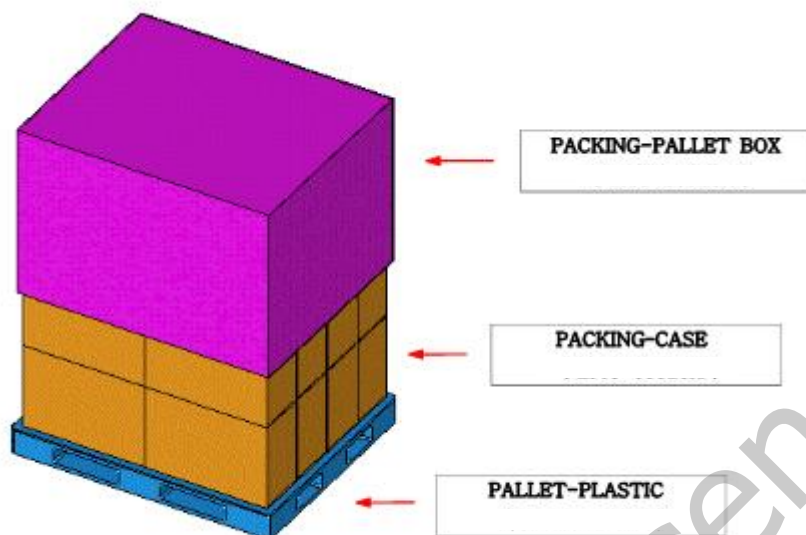
For eyes of Hisense only

8. PACKING

(1) Packing Form

Corrugated Cardboard box as shock absorber.

(2) Packing Method



Item	Specification	Remark
LCD Packing	12ea / Box	21Kg/ LCD (12ea) 
Pallet	16Box / Pallet	Pallet weight : 8.8Kg
Packing Direction	Vertical	
Total Pallet Size	H x V x height	1270mm(H) x 1072mm(V) x 865mm (h: with pallet)
Total Pallet Weight	345Kg	Pallet + Module + Cushion + Pallet box

9. MARKINGS & OTHERS

A nameplate is affixed to the specified location on each product.

- (1)Parts number : LTA230AN01
- (2)Revision code : 3 letters
- (3)Lot number : XXXXXXXXXXXX

(4) Nameplate Indication



- Parts name : LTA230AN01
- Lot number : XXXXXXXXXXXX
- Week code : 1230 (2012 year 30th week)
- Product Revision Code : 801

(5) Packing small box attach



- Parts name : LTA230AN01
- Box serial number : ZAT53K003

10. GENERAL PRECAUTIONS

10.1 HANDLING

- (a) When the module is assembled, It should be attached to the system firmly using every mounting holes. Be careful not to twist and bend the module.
- (b) Refrain from strong mechanical shock and / or any force to the module. In addition to damage, this may cause improper operation or damage to the module and LED back-light.
- (c) Note that polarizers are very fragile and could be easily damaged. Do not press or scratch the surface harder than a HB pencil lead.
- (d) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, Staining and discoloration may occur.
- (e) If the surface of the polarizer is dirty, clean it using some absorbent cotton or soft cloth.
- (f) The desirable cleaners are water, IPA (Isoprophyl Alcohol) or Hexane. Do not use Ketone type materials(ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
- (g) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth .In case of contact with hands, legs or clothes, it must be washed away thoroughly with soap.
- (h) Protect the module from static , it may cause damage to the C-MOS Gate Array IC.
- (i) Use fingerstalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (j) Do not disassemble the module.
- (k) Do not pull or fold the LED FPC.
- (l) Do not touch any component which is located on the back side.
- (m) Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.
- (n) Pins of I/F connector shall not be touched directly with bare hands.
- (o) Because the converter use high voltage, it should be disconnected from power before it is assembled or disassembled.

10.2 STORAGE

We highly recommend to comply with the criteria in the table below.

ITEM	Unit	Min.	Max.
Storage Temperature	(°C)	5	40
Storage Humidity	(%rH)	35	75
Storage Life	12 months		
Storage Condition	- The storage room should be equipped with a good ventilation facility, which has a temperature controlling system. - Products should be placed on the pallet, which is away from the wall not on the floor. - Prevent products from being exposed to the direct sunlight, moisture, and water.; Be cautious not to pile the products up. - Avoid storing products in the environment, which other hazardous material is placed. - If products are delivered or kept in the storage facility more than 3 months, we recommend you to leave products under the condition including a 20°C temperature and a humidity of 50% for 24 hours. - If you store semi-manufactured products for more than 3 months, bake the products under the condition including the 50°C temp. and the 10% humidity for 24hrs after being used.		

10.3 OPERATION

- (a) Do not connect or disconnect the cable to/ from the module at the "Power On" condition.
- (b) The power shall be always turned on/off by the item 6.5. "Power on/off sequence"
- (c) The module has a circuit with a high frequency. The system manufacturers shall suppress the electromagnetic interference sufficiently. The methods to ground and shield are important to minimize the interference.
- (d) Design the length of cable to connect between the connector for back-light and the Converter as short as possible and the shorter cable shall be connected directly.
The longer cable between that of back-light and that of Converter may cause the luminance of LED package to lower and need a higher startup voltage(Vs).

10.4 Operation Condition Guide

(a) The LCD product should be operated under normal conditions. Normal condition is defined as below;

- Temperature : $20 \pm 15^{\circ}\text{C}$
- Humidity : $55 \pm 20\%$
- Display pattern : continually changing pattern (Not stationary)

(b) If the product will be used in extreme conditions such as high temperature, humidity, display patterns or operation time etc., It is strongly recommended to contact SDC for Application engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at Airports, Transit Stations, Banks, Stock market, and Controlling systems.

10.5 OTHERS

(a) The filter for ultra-violet ray is needed when you operate a product outdoor.

(b) Avoid placing the product in the environment, which water is condensed. The former mentioned condition may lead a product to operate improperly or an electrode to be disconnected.

(c) Do not exceed the ceiling of absolute maximum rating. (Various supply voltages, Various input voltages, Various contents on the part, various environmental temperatures, and so on) Otherwise, the module may be damaged.

(d) If the module displays the same pattern for a long time, the situation can be the image sticking to the screen.

(e) This module shall be handled carefully in order not to be stressed by the object from outside since this module contains a circuitry for PCB on the rear side.

11. Reliability test

Item	Test condition	Quantity
TSS	-20℃ ~ 65℃, 440cycle determination	4EA
HTOL	60℃, 500hr determination	4EA
LTOL	-5℃, 500hr determination	4EA
HTS	70℃, 500hr determination	4EA
LTS	-25℃, 500hr determination	4EA
THB	50℃ / 90%RH, 500Hr determination	4EA
TS	-20℃ 0.5Hr → 60℃ 0.5Hr, 100cycle determination	4EA
ESD (non-operation)	Converter input connector : ±15 kV, 150 pF/330 Ω, 3times/pin LED input connector : ±4 kV, 150 pF/330 Ω, 3times/pin	3EA
ESD(operation)	contact : ±10 kV, 150 pF/330Ω, 210 Point, 1 time/Point air : ± 20kV, 150 pF/330Ω, 210 Point, 1 time/Point	3EA
POWER ON/OFF	-5℃/60℃, 3sec (on) / 2sec(off), 1000 times	4EA
Vibration	10 ~ 300Hz : 1.5G/10minSR, XYZ, 30min/axis [30 ~ 50Hz : 3G/10minSR, XYZ, 30min/axis]	3EA
Shock	11msec (±XYZ : 50G)	3EA
Acoustic Noise	Electromagnetic noise : below 24dB Expansion/ Contraction noise by Heat : Max 50dB (Do not exceed 36dB more than 10times)	2EA
Temperature Humidity Stress	-20℃ ~ 65℃, 0 ~ 90%RH, 2cycle	4EA
PALLET Vibration	5~200Hz , 1.05Grms, Random 2Hr / Y	1PALLET
PALLET Drop	20cm, 2Edge	1PALLET
Complex	WHTS : Pallet Vibration/ Drop → WHTS → THB 48Hr HTS, LTS : Pallet Vibration/ Drop → HTS, LTS → HTOL 48Hr	WHTS 10EA HTS 4EA LTS 4EA

[Criteria on evaluation]

The components of product, which may affect to the function of display shall not be changed when the display quality test is executed under the normal operating condition.

- * HTOL/ LTOL : The operating at the high and low temperature*
- * THB : The slant of temperature and humidity
- * HTS/ LTS : The storage at the high and low temperature
- * WHTS : The storage condition at the high temperature with the high humidity